Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)
BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Date: 4.9.17.

From
Dr. T.R. Rangaswamy,
Professor & Head,
Department of EEE,
Bharath Institute of Higher Education and Research,
Chennai

To
The Pro VC-Academics,
Bharath Institute of Higher Education and Research,
Chennai

Respected sir

Subject : Request of Permission to conduct a value added course on "**Course on Smart Electric Grid**" -Reg

With reference to above subject, I would like to bring to your kind notice that, our department interested to organize value added course on "Application of PSCAD and Transient studies" in our campus premises on **26.9.17**.

Mr. Muneeswaran, of Tools TVS Training & Services, Chennai 600 058, would deliver lecture for the above mentioned course. 45 students would be participating in this course. We request you kindly to give permission to organize this event.

Venue: **EEE seminar Hall**

Timing : **8 am to 6.00 pm**

Submitted to Principal for approval to organize this value added course.

Yours sincerely

Copy to ADR/COE

Head of the Department
Department of E.E.E.
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
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No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 6.9.17

A value added course on **"Course on smart Electric Grid"** is planned by the School of Electrical Engineering on **26.09.2017.** In this regard, students are instructed to give their willingness and confirm their participation to their respective class in charge before 21.9.2017. Course commences on 26.9.17 and it would be conducted for three days **(26.9.2017, 27.9.2017& 28.9.2017**) from 8.00 AM to 6.00 P.M.

Bharath Institute of Higher Education & Research (Declared as Deemed to be University U/S 3 of UGC Act 1956) Dept. of E.E.E. Chennai-73

HOD

Head of the Department
Department of E.E.E.
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
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Copy to
Department of ECE
Notice Board/ Department of EEE

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INSTITUTE OF HIGHER EDUCATION AND RESEARCH
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SCHOOL OF ELECTRICAL ENGINEERING

Course on smart Electric Grid

SCHEDULE

Contact Hours : 32 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
26.9.17	FN	9.00 am to 12.30 pm	• Introduction to power system operation • Today's grid and the smart grid • Application of computational intelligence in smart grids • Enhancing power system functionality by smart grids • Communications in smart grids	Mr. Muneeswaran
	AN	1.30 pm to 4 pm	• Smart Grid Communications And Measurements • Communication and measurements • Phasor measurement units • Smart meters • Measurement technologies in smart grids • Global information system (GIS) • Google mapping tools • Multi-agent systems (MAS)	Mr. Muneeswaran

			• Microgrids	
27.9.17	FN	9.00 am to 12.30 pm	• Smart Grid Analysis • Load flow analysis • Challenges to load flow in smart grids • Congestion management • Optimal power flow in smart grids • Contingencies in smart grids • Contingency studies in smart grids	Dr.S.P.Vijayaragavan
	AN	1.30 pm to 4 pm	• Advanced Smart Grids For Distribution System Operators • NERC Requirements • Reliability Consideration of Smart Grid • Integration of Renewable Energy Sources • Demand Response • Introduction to computational tools in smart grids • Optimization techniques in smart grids	Dr.V.Jayalakshmi
28.9.17	FN	9.00 am to 12.30 pm	• Renewable Energy And Storage • Renewable energy resources • Solar generation • Wind generation • Energy storage in smart grids	Mr. Muneeswaran
	AN	1.30 pm to 4 pm	• INTEROPERABILITY AND STANDARDS IN SMART GRIDS • Interoperability • Standards in smart grids	Mr. Muneeswaran

			• Smart grid cyber security	
29.9.17	FN	9.00 am to 12.30 pm	• SMART GRID CONTROL • Control of solar generation • Solar inverters • Smart inverters •	Dr.S.Prakash
	AN	1.30 pm to 5 pm	• Hands on experiences	Dr.S.P.Vijayaragavan
30.9.17	FN	9.00 am to 12.30 pm	• Grid integration challenges and prospective solutions	Dr.S.Prakash
	AN	1.30 pm to 5 pm	• Grid-forming control • Grid-supporting control	Dr.S.P.Vijayaragavan

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ABET

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

The following students attended Three days' workshop on **"Course on smart Electric Grid"** On 26.09.2017, 27.09.2017and 28.09.2017 at **Tools TVS Training & Services, Chennai.**

S.No	Register No	Name of the student	Signature of the Student		
			26.09.2017	27.09.2017	29809.2017
1.	U14EE001	ABENASH.R			
2.	U14EE002	ABHISHEK KUMAR			
3.	U14EE003	AJAY KUMAR MISHRA			
4.	U14EE004	AJMEERA. SWAPNA			
5.	U14EE005	AMIT KUMAR			
6.	U14EE006	ANURAG RAJ			
7.	U14EE007	BARUN KUMAR CHAKRABORTY			
8.	U14EE008	ANNARAM BHASKER GOUD			
9.	U14EE009	CHANDAN KR PANDIT			
10.	U14EE010	CHHOTEISHWAR NALLAMANTI			
11.	U14EE011	CHINTHAPARTHI SIVASANKAR			
12.	U14EE012	DHARANALAKOTA VENKATASATYA CHITTI SWARUP			
13.	U14EE014	INAPARTHI SWATHI			
14.	U14EE015	ISHTIYAQ BASHIR			
15.	U14EE016	KAMISETTY SAI VISHNU			
16.	U14EE017	KHUMANTHEM DENIM SINGH			
17.	U14EE018	KUNDAN KUMAR			

18.	U14EE019	LANKELA SRINIVASULU			
19.	U14EE020	LAYEEK ASHAD			
20.	U14EE021	MD INAM			
21.	U14EE022	MEHBOOB HUSSAIN			
22.	U14EE023	MUTHYALA HEMANTH SAI			
23.	U14EE024	NAINA MOHAMMED			
24.	U14EE025	KOYA NARESH.			
25.	U14EE026	N. VIJAYA SEKHAR			
26.	U14EE027	NITESH PRASAD			
27.	U14EE028	NITISH KUMAR			
28.	U14EE029	NOOR MOHAMED.M			
29.	U14EE030	PALAPARTHI TIRUMALA RAO			
30.	U14EE031	PINNAMANENI PRAVEEN KUMAR			
31.	U14EC017	ASARA ANITH RAO			
32.	U14EC018	BANKIM CHANDRA BHARTI			
33.	U14EC019	BEDDINTI PRAVEEN KUMAR			
34.	U14EC020	BETHALA MOURYA			
35.	U14EC021	BOJJA. VENKATA PRASAD			
36.	U14EC022	BOYAPATI PUSHYAMITHRA			
37.	U14EC023	CHALUVADI DIVYA BHARATHI			
38.	U14EC024	CHANDRALEKA.K			
39.	U14EC025	CHEKURI.VENKATA MAHESH			
40.	U14EC029	DEENADHAYALAN.A			
41.	U14EC030	DEEPAK KUMAR			
42.	U14EC031	DEEPAK.A			
43.	U14EC034	DOLLY NISHA J.S.			
44.	U14EC035	DUVVURU SREENIVASA TEJA			
45.	U14EC038	GADDI TEJA RAM			

Signature of HOD

Head of the Department
Department of E.E.E.
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ABET

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE - Course on smart Electric Grid

FEEDBACK FORM					Date: 28.9.2017
Name	Chinthaparthi Sivasankar.				
Register Number	014EE011				
Phone Number	9642191872				
Email address	Sivaskankar3245@gmail.com				
	Poor	**Fair**	**Good**	**Very Good**	**Excellent**
Overall Program			✓		
Resource person				✓	
Audio visual aids & Technology used				✓	
Presentation Handouts				✓	

Student's Signature

Bharath
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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

ABET

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE - Course on smart Electric Grid

FEEDBACK FORM					Date: 28.9.2017
Name	Kamisetty saivishnu				
Register Number	11706016				
Phone Number	9082084042				
Email address	saivishnu1111@gmail.com				
	Poor	**Fair**	**Good**	**Very Good**	**Excellent**
Overall Program					✓
Resource person				✓	
Audio visual aids & Technology used			✓		
Presentation Handouts				✓	

Kamisetty

Student's Signature

Bharath

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NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **KUNDAN KUMAR (U14EE018)**

has attended Value added Course On ***"Course on smart Electric Grid"*** organized by the School of Electrical Engineering, BIHER conducted from 26.09.2017 to 28.09.2017.

COURSE COORDINATOR

CONVENOR

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
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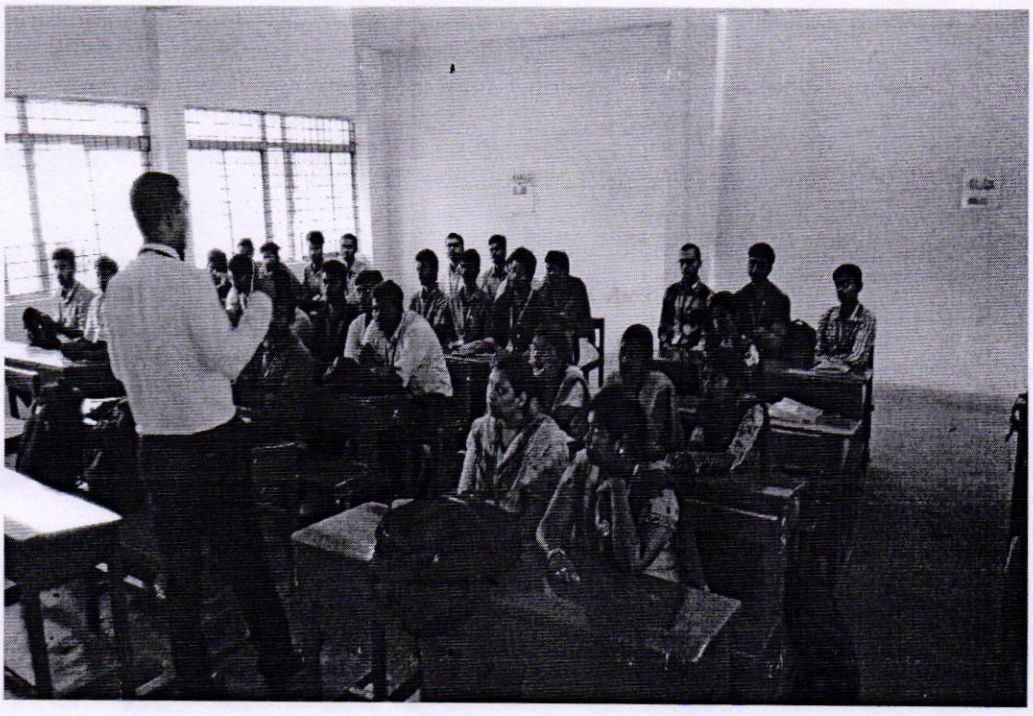
SCHOOL OF ELECTRICAL ENGINEERING

Course on smart Electric Grid dated on *26.09.2017, 27.09.2017and 28.09.2017* conducted by school of Electrical Engineering



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From
Dr. T.R. Rangaswamy,
Professor & Head,
Department of EEE,
Bharath Institute of Higher Education and Research,
Chennai

Date: 3.11.17

To
The Pro VC-Academics,
Bharath Institute of Higher Education and Research,
Chennai

Respected sir

Subject : Request of Permission to conduct a value added course on "**Embedded System Design and Development** " -Reg

With reference to above subject, I would like to bring to your kind notice that, our department interested to organize value added course on "Application of PSCAD and Transient studies" in our campus premises on **27/11/2017**.

Dr. G.Thyagu, Managing Director New BEE Technologies, Chennai 600 101, would deliver lecture for the above mentioned course. 60 students would be participating in this course. We request you kindly to give permission to organize this event.

Venue: **EEE seminar Hall**

Timing : **8 am to 6.00 pm**

Submitted to Principal for approval to organize this value added course.

Yours sincerely

Copy to ADR/COE

Head of the Department
Department of E.E.E.
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CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 7.11.17

A value added course on **"Embedded System Design and Development"** is planned by the School of Electrical Engineering on **27.11.2017.** In this regard, students are instructed to give their willingness and confirm their participation to their respective class in charge before 22.11.2017. Course commences on 27.11.17 and it would be conducted for three days **(27.11.2017, 28.11.2017 & 29.11.2017)** from 8.00 AM to 6.00 P.M.

Bharath Institute of Higher Education & Research (Deemed to be University U/S 3 of UGC Act 1956) Dept. of E.E.E. Chennai-73

HOD

Head of the Department
Department of E.E.E.
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SCHOOL OF ELECTRICAL ENGINEERING

Embedded System Design and Development

SCHEDULE

Contact Hours : 32 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
27.11.17	FN	9.00 am to 12.30 pm	Embedded C Language 'C' Language basics, Data Embedded C Language,' C' Language basics element using Pointers, Concepts of Embedded C, Architecture of 8051 Family of Microcontrollers,	Dr. G.Thyagu
	AN	1.30 pm to 4 pm	Assembly Language Programming of 8051,EmbeddedC programming of 8051,On-Chip Timers Programming Examples	Dr. G.Thyagu
28.11.17	FN	9.00 am to 12.30 pm	Embedded C Language, 'C' Language basics, Data Embedded C Language, 'C' Language basics element using Pointers, Concepts of Embedded C	Dr.T.R.Rangaswamy
	AN	1.30 pm to 4 pm	Architecture of 8051 Family of Microcontrollers, Assembly Language Programming of 8051,EmbeddedC programming of 8051,On-ChipTimers,Programming Examples	Dr.T.R.Rangaswamy

29.11.17	FN	9.00 am to 12.30 pm	Microcontroller interfacing techniques, Sensors and its types,	Dr.S.P.Vijayaragavan
	AN	1.30 pm to 4 pm	Analog-to-Digital Converters, Data acquisition from sensors using Microcontroller, Programming Examples	Dr.T.R.Rangaswamy
30.11.17	FN	9.00 am to 12.30 pm	Simple LED program, LED blinking	Dr.S.P.Vijayaragavan
	AN	1.30 pm to 5 pm	Interfacing with IR Sensor, Interfacing with LCD	Dr.T.R.Rangaswamy
1.12.17	FN	9.00 am to 12.30 pm	Interfacing with servo motor, Interfacing with Bluetooth module,	Dr. G.Thyagu
	AN	1.30 pm to 5 pm	Interfacing with Wifi Module, Control of Electronic Devices using Android smart phone and Arduino	Dr. G.Thyagu

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

The following students attended Three days' workshop on "Embeded System Design and Tools" On 27.11.2017, 28.11.2017 and 29.11.2017 at **New BEe Technologies, Chennai.**

S.No	Register No	Name of the student	Signature of the Student		
			27.11.2017	28.11.2017	29.11.2017
1.	U14EE001	ABENASH.R			
2.	U14EE002	ABHISHEK KUMAR			
3.	U14EE003	AJAY KUMAR MISHRA			
4.	U14EE004	AJMEERA. SWAPNA			
5.	U14EE005	AMIT KUMAR			
6.	U14EE006	ANURAG RAJ			
7.	U14EE007	BARUN KUMAR CHAKRABORTY			
8.	U14EE008	ANNARAM BHASKER GOUD			
9.	U14EE009	CHANDAN KR PANDIT			
10	U14EE010	CHHOTEISHWAR NALLAMANTI			
11	U14EE011	CHINTHAPARTHI SIVASANKAR			
12	U14EE012	DHARANALAKOTA VENKATASATYA CHITTI SWARUP			

13	U14EE014	INAPARTHI SWATHI			
14	U14EE015	ISHTIYAQ BASHIR			
15	U14EE019	LANKELA SRINIVASULU			
16	U14EE020	LAYEEK ASHAD			
17	U14EE021	MD INAM			
18	U14EE022	MEHBOOB HUSSAIN		-AB-	
19	U14EE023	MUTHYALA HEMANTH SAI		AB	
20	U14EE026	N. VIJAYA SEKHAR			
21	U14EE027	NITESH PRASAD			
22	U14EE028	NITISH KUMAR			-AB-
23	U14EE031	PINNAMANENI PRAVEEN KUMAR			
24	U14EE033	PRIYESH KUMAR PANDEY			
25	U14EE034	RAJ KISHOR DAS		-AB-	
26	U14EE035	RAJ KUNDAN			AB
27	U14EE036	RAJNEESH KUMAR			
28	U14EE038	RAKESH V			
29	U14EE039	RAVI KANT			
30	U14EE053	VINOD KUMAR MURMU			
31	U14EE054	YOGESHWARAN .D			
32	U14EE055	RAMASAMY.R			
33	U14EE056	AMIT AMAN			
34	U14EE057	RAYSHETTI KARTHIK BABU			
35	U14EE058	TAMESHWAR BANJARE			
36	U14EE059	NAMBALLA MADHU BABU			
37	U14EE060	ADIKI SWARNA			-AB-
38	U14EE501	SATHISH KINGSLY JEBARAJ A			
39	U14EE701	MAHENDRAN M			-AB-

40	U14EE702	DINESH KUMAR P		AB	
41	U14EE703	VENKATESAN R		- AB -	
42	U14EE704	LINS REXINE D			
43	U14EE705	B. RUZO			
44	U14EE706	ARVIND M			- AB -
45	U14EE707	KUNTAL GHOSH			
46	U14EE708	MOAZZAM ALI			
47	U14EE709	FAHAD A			- AB -
48	U15EE012	GOWTHAM K			
49	U15EE013	HEMANT KUMAR SAHU			
50	U14EC013	ANKIT KAUSHAL			
51	U14EC014	ANNAPANENI VAMSIDHAR			
52	U14EC015	ANUSHA.R			AB
53	U14EC016	ARCHANA.R			
54	U14EC017	ASARA ANITH RAO		- AB -	
55	U14EC018	BANKIM CHANDRA BHARTI			
56	U14EC019	BEDDINTI PRAVEEN KUMAR			
57	U14EC020	BETHALA MOURYA		AB	
58	U14EC021	BOJJA. VENKATA PRASAD			AB
59	U14EC022	BOYAPATI PUSHYAMITHRA			
60	U14EC023	CHALUVADI DIVYA BHARATHI			
61	U14EC024	CHANDRALEKA.K			
62	U14EC025	CHEKURI.VENKATA MAHESH			- AB -
63	U14EC026	CHINTA ANVESH			
64	U14EC027	DAMMALAPATI RAHUL DIVYESH			
65	U14EC040	GARAGA SIVA SURYA DEEPAK		AB	

66	U14EC041	S GOKUL			
67	U14EC042	GOURU VENKATA SAI PRAKASH			
68	U14EC043	GOVINDUGARI NITHIN REDDY			
69	U14EC044	GUJJARI SHIVADURGA PRASAD			
70	U14EC045	GULAM AHMED REJA			
71	U14EC046	GUMMADAVELLI VINAY			
72	U14EC047	GUNDA VENKATA MEHAR VINAY KUMAR			
73	U14EC048	GUTTA MOHAN RANGA RAO			

Signature of HOD

Head of the Department
Department of E.E.E.
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
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A NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **YOGESHWARAN .D (U14EE054)**

has attended Value added Course On ***"Embeded System Design and Tools"*** organized by the School of Electrical Engineering, BIHER conducted from 27.11.2017 to 29.11.2017.

COURSE COORDINATOR

CONVENOR

Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

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ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **INAPARTHI SWATHI (U14EE014)**

has attended Value added Course On ***"Embeded System Design and Tools"*** organized by the School of Electrical Engineering, BIHER conducted from 27.11.2017 to 29.11.2017.

COURSE COORDINATOR

CONVENOR

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
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SCHOOL OF ELECTRICAL ENGINEERING

Embeded System Design and Tools dated on *27.11.2017, 28.11.2017 and 29.11.2017* conducted by school of Electrical Engineering

Bharath
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SCHOOL OF ELECTRICAL ENGINEERING

Value Added Courses (2017 -2018)

Familiarization of Matlab and simulation

Course Objective

MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming environment. Furthermore, MATLAB is a modern programming language environment ,it has sophisticated data structures, contains built-in editing and debugging tools, and supports object-oriented programming. These factors make MATLAB an excellent tool for teaching and research. MATLAB has many advantages compared to conventional computer languages (e.g., C, FORTRAN) for solving technical problems. MATLAB is an interactive system whose basic data element is an array that does not require dimensioning. The software package has been commercially available since 1984 and is now considered as a standard tool at most universities and industries worldwide.

Resource Persons :

1.Ms.M.Jasmin

2.Ms.B.Hemalatha

3.Ms.S.Philomina

Convener

Dr.M.Sangeetha

HOD/ECE

DEPT OF ECE

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
Declared as Deemed-to-be-University u/s 3 of the UGC Act, 1956

CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 20.11.2017

The course on Familiarization of Matlab and simulation is planned by School of Electrical Engineering which commences on 18-12-17 (Monday). In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

INSTITUTE OF HIGHER EDUCATION AND RESEARCH
DEPT of ECE

To,
Copy to ECE Department,
Copy to EEE Department,
Department Notice Board

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
Declared as Deemed-to-be-University u/s 3 of the UGC Act, 1956

SCHOOL OF ELECTRICAL ENGINEERING

Familiarization of Matlab and simulation

SCHEDULE

Contact Hours : 31 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
18-12-2017	FN	9.00 am to 12.30 pm	Introduction to MATLAB Software MATLAB window Command window Workspace Command history Setting directory Working with the MATLAB user interface	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Character and string Arrays and vectors Column vectors Row vectors	Ms.S.Philomina
19-12-2017	FN	9.00 am to 12.30 pm	BODMAS Rules Arithmetic operations Operators and special characters Mathematical and logical operators Solving arithmetic equations	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Crating rows and columns Matrix Matrix operations Finding transpose, determinant and inverse Solving matrix	Ms.M.Jasmin
20-12-2017	FN	9.00 am to 12.30 pm	Trigonometric functions Complex numbers fractions Real numbers Complex numbers	Ms.S.Philomina
	AN	1.30 pm to 4 pm	Working with script tools Writing Script file	Ms.M.Jasmin

			Executing script files The MATLAB Editor Saving m files	
21-12-2017	FN	9.00 am to 12.30 pm	Plotting vector and matrix data Plot labelling, curve labelling and editing	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Basic Plotting Functions Creating a Plot Plotting Multiple Data Sets in One Graph Specifying Line Styles and Colors Graphing Imaginary and Complex Data Figure Windows Displaying Multiple Plots in One Figure	Ms.S.Philomina
22-12-2017	FN	9.00 am to 12.30 pm	Creating Mesh and Surface About Mesh and Surface Visualizing Subplots	Ms.M.Jasmin
	AN	1.30 pm to 5 pm	Introduction Of Simulink Simulink Environment & Interface Study of Library Circuit Oriented Design Equation Oriented Design	Ms.S.Philomina

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Familiarization of Matlab and simulation

List Of Participants

Date:18.12.2017

Sl.no	REG.NO	NAME OF THE CANDIDATE
1	U14EC001	AAKAASH THAKUR
2	U14EC003	ABBISETTY SAI NIHARIKA
3	U14EC010	AMARJEET KUMAR
4	U14EC013	ANKIT KAUSHAL
5	U14EC016	ARCHANA.R
6	U14EC017	ASARA ANITH RAO
7	U14EC018	BANKIM CHANDRA BHARTI
8	U14EC019	BEDDINTI PRAVEEN KUMAR
9	U14EC020	BETHALA MOURYA
10	U14EC025	CHEKURI.VENKATA MAHESH
11	U14EC026	CHINTA ANVESH
12	U14EC033	N DHEERAJ
13	U14EC034	DOLLY NISHA J.S.
14	U14EC040	GARAGA SIVA SURYA DEEPAK
15	U14EC041	S GOKUL
16	U14EC042	GOURU VENKATA SAI PRAKASH
17	U14EC066	MANAM KOKILA.
18	U14EC067	KOMMANI DIVYA SREE

19	U14EC068	KOMMIDI PUNNAM CHANDER
20	U14EC072	KONDURU PAVAN SAI
21	U14EC073	KOTA VIDYA SAGAR
22	U14EC075	SINGAMALA MALLIKARJUNA REDDY
23	U14EC081	MAYANK HARSHIT
24	U14EC082	MD.FAIYAZ ALAM
25	U14EC083	MEENAAKSHI S
26	U14EC089	MUDRAKOLLA SURESH SACHIN
27	U14EC090	MUTYALA SAI HARISHITHA
28	U14EC103	PAPUGANI PARTHASARADHI.
29	U14EC104	PEDINEEDI VIJAYA BHARGAVI
30	U14EC106	PENGALAPATI BHARATHI
31	U14EC113	PUNUGOTI ANUSHA
32	U14EC114	RACHAMADUGU MANISH
33	U14EC121	SALUMURI RAVI TEJA
34	U14EC123	CHEEDELLA SARACCHANDRA.
35	U14EC128	SHAIK.ALEEM

(Dr.M.Sangeetha)

HOD/ECE

DEPT of ECE

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
Declared as Deemed-to-be-University u/s 3 of the UGC Act, 1956

SCHOOL OF ELECTRICAL ENGINEERING

Course on **Familiarization of Matlab and simulation** dated on 18.12.2017 conducted by School of Electrical Engineering

Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed - to - be - University under section 3 of UGC Act 1956)

NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **ANKIT KAUSHAL(U14EC013)**

has attended Value added Course On "***Familiarization of Matlab and Simulation***" organized by the School of Electrical Engineering, BIHER conducted from 18-12-2017 to 22-12-2017.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Familiarization of Matlab and simulation

FEED BACK FORM					Date: 22/12/17
Name	Nitish Kumar				
Register number	U14EE028				
Phone number	7358123 09				
Email address	nitish2009@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker			✓	✓	
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Familiarization of Matlab and simulation

FEED BACK FORM					Date: 22/12/17
Name	Meenakshi.S				
Register number	U14EC083				
Phone number	8844655 93				
Email address	meenumathi@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker			✓		
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Meen
Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

Fundamentals of Micro and NanoFabrication

Value Added Courses-2018

Course Objective

The objective of this course of study is to provide students with a glimpse into the semiconductor industry that has been the foundation upon which the electronics industry has been based for the past half century, and to provide insight into the future of that industry as well as nanotechnology in general. In the last 50 years, the dimensions of the features built into integrated circuits have shrunk from 25 mm to 25 nm. Over the next decade these features will approach atomic dimensions, giving rise to a host of unique nanotechnology challenges and opportunities.

The definition and description of the terminology and processes of microelectronics; semiconductor facilities and chemical processes for integrated circuit manufacture with an emphasis upon unit processes; the major unit processes including thin-film metal and dielectric deposition and etching, silicon oxidation and etching, ion implantation, diffusion, lithography, and planarization; an overview of promising nano patterning and nanofabrication techniques, such as electron and other particle-beam imaging, nanoimprint, and near-field probe imaging.

Resource Persons :

1.Ms.S.Saravana

2.Ms.K.Subbulakshmi

3.Ms.B.Hemalatha

Convener

Dr.M.Sangeetha

HOD/ECE

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CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 2.02.2018

The course on Fundamentals of Micro and Nano Fabrication is planned by School of Electrical Engineering which commences on 01.03.2018(Wednesday).In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

DEPT of ECE

To,
Copy to ECE Department,
Copy to EEE Department,
Department Notice Board

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SCHOOL OF ELECTRICAL ENGINEERING

Fundamentals of Micro and NanoFabrication

SCHEDULE

Contact Hours : 31 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
01.03.2018	FN	9.00 am to 12.30 pm	Tunnel junction and applications of tunneling, Tunneling Through a Potential Barrier, Metal—Insulator, Metal-Semiconductor, and Metal-Insulator-Metal Junctions, Coulomb Blockade, Tunnel Junctions	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Tunnel Junction Excited by a Current Source. Spintronics and Foundations of nano-photonics.	Ms.S.Saravana
02.03.2018	FN	9.00 am to 12.30 pm	Field Emission, Gate—Oxide Tunneling and Hot Electron Effects in nano MOSFETs, Theory of Scanning Tunneling Microscope, Double Barrier Tunneling and the Resonant Tunneling Diode.	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Introduction to lithography- Contact, proximity printing and Projection Printing, Resolution Enhancement techniques, overlay-accuracies, Mask-Error enhancement factor (MEEF), Positive and negative photoresists, Electron Lithography, Projection Printing, Direct	Ms.K.Subbulakshmi

			writing,	
03.03.2018	FN	9.00 am to 12.30 pm	Electron resists. Lithography based on Surface Instabilities: Wetting, De-wetting, Adhesion, Limitations, Resolution and Achievable / line widths etc. Lift off process, Bulk Micro machining.	Ms.S.Saravana
	AN	1.30 pm to 4 pm	Introduction to MEMS and NEMS, working principles, as micro sensors (acoustic wave sensor, biomedical and biosensor, chemical sensor, optical sensor, capacitive sensor, pressure sensor and thermal sensor), micro actuation (thermal actuation, piezoelectric actuation and electrostatic actuation–micro gripers, motors, valves, pumps, accelerometers	Ms.K.Subbulakshmi
06.03.2018	FN	9.00 am to 12.30 pm	fluidics and capillary electrophoresis, active and passive micro fluidic devices, Pizoresistivity,Pizoelectricity and thermoelectricity, MEMS/NEMS design, processing, Oxidation, Sputter deposition, Evaporation, Chemical vapor deposition etc.	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Introduction – Scaling of physical systems – Geometric scaling & Electrical system scaling.	Ms.S.Saravana
07.03.2018	FN	9.00 am to 12.30 pm	The Single-Electron Transistor: The Single- Electron Transistor Single-Electron Transistor Logic, Other SET and FET Structures,	Ms.K.Subbulakshmi
	AN	1.30 pm to 5 pm	Carbon Nanotube Transistors (FETs and SETs), Semiconductor Nanowire FETs and SETs,Coulomb Blockade in a Nanocapacitor, Molecular SETs and Molecular Electronics.	Ms.S.Saravana

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Fundamentals of Micro and NanoFabrication

List Of Participants

Date:01.03.2018

Sl.no	REG.NO	NAME OF THE CANDIDATE
1	U15EC002	AKHIL CHELLUBOINA
2	U15EC003	AKULA SUJITH KRISHNA
3	U15EC004	ALOK KUMAR
4	U15EC005	ALUVALA ARUN KUMAR GOUD
5	U15EC006	AMAYA E
6	U15EC007	AMBULA DEVI GOWTHAM
7	U15EC009	AMMISETTI AVINASH
8	U15EC010	ANKIT KUMAR DUBEY
9	U15EC015	ATTAR MOHAMMED TOUSIF
10	U15EC016	ATUKURI AVINASSH
11	U15EC017	BASETTY HIMABINDU
12	U15EC018	BOJJA PHANINDHRA REDDY
13	U15EC019	C. SHIVARAMAN SRIKANTH
14	U15EC020	CHANDAN PANDAY
15	U15EC021	CHAPARTHI KARTHIK
16	U15EC022	CHEKKA KESAVA PRAJWAL
17	U15EC023	CHITTIBOMMA SWATHI
18	U15EC024	DASARI HARI SAI KUMAR

19	U15EC025	DUDEKULA FAYAZ
20	U15EC026	DUDEKULA NOORNIYAZ
21	U15EC027	DUGYALA PREETHI
22	U15EC028	FAHIMA NASREEN S
23	U15EC030	GADE MOUNIKA
24	U15EC042	JAKKU MANIDEEP
25	U15EC044	JETTY SAI SUDHEER
26	U15EC046	JONNALAGADDA VENKATA MANOJ KUMAR
27	U15EC047	K O HARICHANDANA
28	U15EC050	KARICHETI BALAKRISHNA
29	U15EC051	KARNAM MOHITH
30	U15EC053	KELAM PHANI SHANKAR
31	U15EC056	KOMURAVELLI ABHILASH
32	U15EC057	KONDA ANANTH REDDY
33	U15EC058	KONDA SANDEEP
34	U15EC059	KONDAMURI VENKATESH
35	U15EC061	KONREDDY HARITHA

(Dr.M.Sangeetha)

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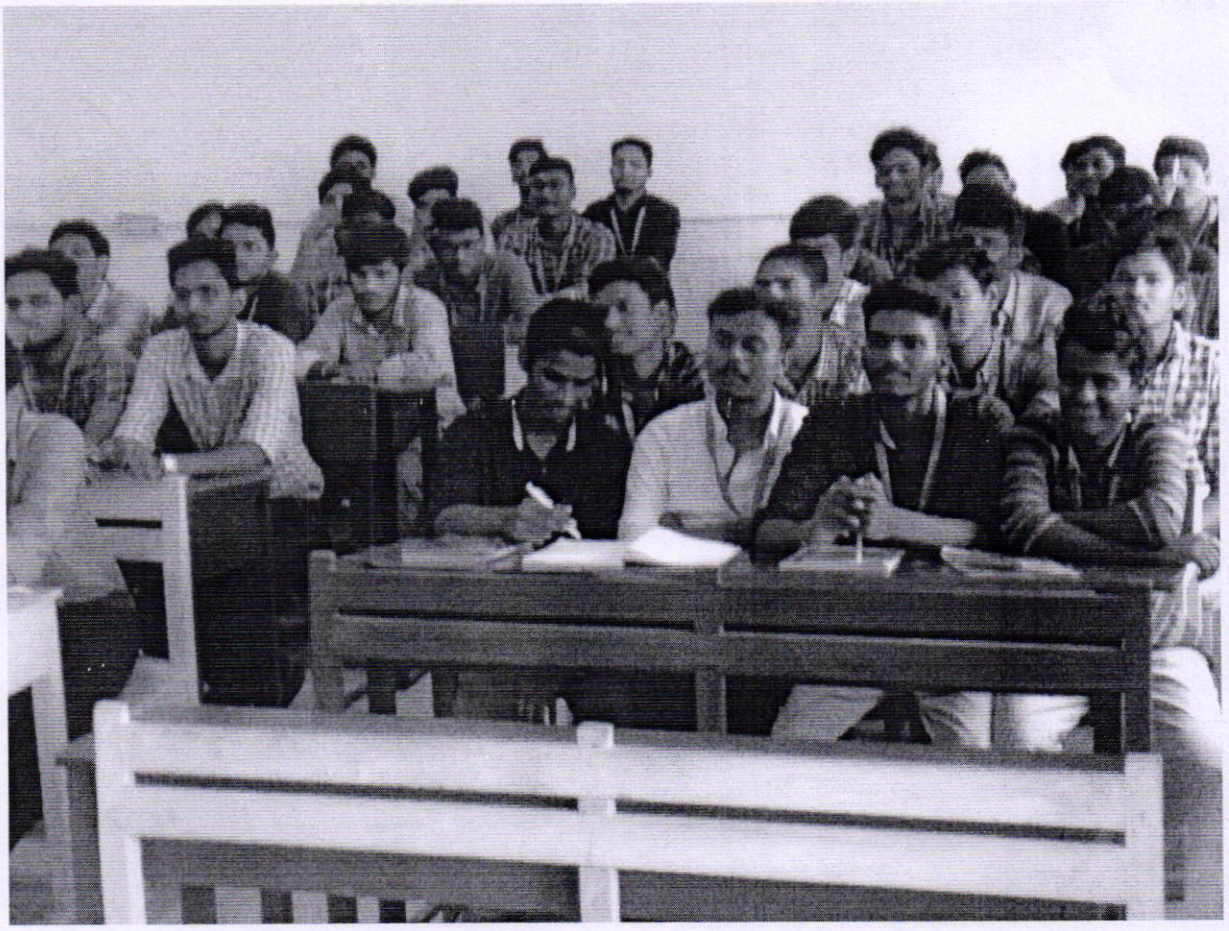
SCHOOL OF ELECTRICAL ENGINEERING

Course on Fundamentals of Micro and NanoFabrication dated on 01.03.2018 conducted by School of Electrical Engineering



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SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **KONREDDY HARITHA(U15EC061)** has attended Value added Course On ***"Fundamentals Of Micro And NanoFabrication"*** organized by the School of Electrical Engineering, BIHER conducted from 01-03-2018 to 07-03-2018.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

Bharath
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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Fundamentals of Micro and NanoFabrication

FEED BACK FORM **Date:** 07/3/2018

Name	KO. Harichandana
Register number	U15EC047
Phone number	9176415710
Email address	Hari_123@gmail.com

	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker				✓	
Audio,Visual Aids Technology used					✓
Presentation hand outs				✓	

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Fundamentals of Micro and NanoFabrication

FEED BACK FORM **Date:** 07/3/2018

Name	Jayant kumar
Register number	V15EE014
Phone number	9416578901
Email address	kumarbudoly@yahoo.com

	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	✓
TheSpeaker					✓
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Student Signature

Bharath
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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selaiyur, Chennai , T.N - 600 073.

Date: 24.11.17

From
Dr. T.R. Rangaswamy,
Professor & Head,
Department of EEE,
Bharath Institute of Higher Education and Research,
Chennai

To
The Pro VC-Academics,
Bharath Institute of Higher Education and Research,
Chennai

Respected sir

Subject : Request of Permission to conduct a value added course on "**Industrial Applications of PLC and SCADA** " -Reg

With reference to above subject, I would like to bring to your kind notice that, our department interested to organize value added course on "Application of PSCAD and Transient studies" in our campus premises on **18.12.17**.

Ms. Bhuvana & Mr. Rajesh, Design Engineer in Progyaan Automation Centre, Chennai would deliver lecture for the above mentioned course. 65 students would be participating in this course. We request you kindly to give permission to organize this event.

Venue: **EEE seminar Hall**

Timing : **8 am to 6.00 pm**

Submitted to Principal for approval to organize this value added course.

Yours sincerely

Copy to ADR/COE

Head of the Department
Department of E.E.E.
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
(Declared as Deemed to be University U/S 3 of UGC Act. 1956)
Chennai-600 073. INDIA.

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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No.173, Agharam Road, Selalyur, Chennai , T.N - 600 073.

CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 28.11.17

A value added course on **"Industrial Applications of PLC and SCADA"** is planned by the School of Electrical Engineering on **18.12.17.** In this regard, students are instructed to give their willingness and confirm their participation to their respective class in charge before 13.12.2017. Course commences on 18.12.17 and it would be conducted for three days **(18.12.17, 19.12.17 & 20.12.17)** from 8.00 AM to 6.00 P.M.

Bharath Institute of Higher Education & Research (Declared as Deemed to be University U/S 3 of UGC Act, 1956) Dept. of E.E.E. Chennai-73

HOD

Head of the Department
Department of E.E.E.
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
(Declared as Deemed to be University U/S 3 of UGC Act. 1956)
Chennai-600 073. INDIA.

Copy to
Department of ECE
Notice Board/ Department of EEE

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SCHOOL OF ELECTRICAL ENGINEERING

Industrial Applications of PLC and SCADA

SCHEDULE

Contact Hours : 32 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
18.12.17	FN	9.00 am to 12.30 pm	Recent Trends in Industrial Automation & PLC-SCADA	Ms. Bhuvana
	AN	1.30 pm to 4 pm	PLC Programming	Ms. Bhuvana
19.12.17	FN	9.00 am to 12.30 pm	Detail study of PLC & SCADA	Mr. Rajesh
	AN	1.30 pm to 4 pm	Project Application Explanation & Hands-On Experiments	Mr. Rajesh
20.12.17	FN	9.00 am to 12.30 pm	PLC I/Os Basics, Burning & Interfacing Concepts	Dr.S.Prakash
	AN	1.30 pm to 4 pm	Project Application Explanation & Hands-On Experiments	Dr.V.Jayalakshmi
21.12.17	FN	9.00 am to 12.30 pm	SCADA Programming- Basic & Advanced	Dr.S.P.Vijayaragavan
	AN	1.30 pm to 5 pm	Project Application Explanation & Hands-On Experiments	Dr.S.Prakash
22.12.17	FN	9.00 am to 12.30	Sensors Proximity Sensor -- Inductive	Dr.V.Jayalakshmi

		pm	Sensor,-- IR Sensor,Limit Switch/ Level Switch	
	AN	1.30 pm to 5 pm	Project Application Explanation & Hands-On Experiments	Dr.S.P.Vijayaragavan

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ABET

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

The following students attended Three days' workshop on "Industrial Applications of PLC and SCADA" from 18.12.2017, 19.12.2017 and 20.12.2017 at Progyaan Automation Centre, Chennai.

S.No	Register No	Name of the student	Signature of the Student		
			18.12.2017	19.12.2017	20.12.2017
1.	U14EE001	ABENASH.R			
2.	U14EE002	ABHISHEK KUMAR			
3.	U14EE003	AJAY KUMAR MISHRA			
4.	U14EE004	AJMEERA. SWAPNA			
5.	U14EE005	AMIT KUMAR			
6.	U14EE006	ANURAG RAJ			
7.	U14EE007	BARUN KUMAR CHAKRABORTY			
8.	U14EE008	ANNARAM BHASKER GOUD			
9.	U14EE009	CHANDAN KR PANDIT			
10.	U14EE010	CHHOTEISHWAR NALLAMANTI			
11.	U14EE011	CHINTHAPARTHI SIVASANKAR			
12.	U14EE012	DHARANALAKOTA VENKATASATYA CHITTI SWARUP			
13.	U14EE014	INAPARTHI SWATHI			
14.	U14EE015	ISHTIYAQ BASHIR			

15.	U14EE016	KAMISETTY SAI VISHNU			
16.	U14EE017	KHUMANTHEM DENIM SINGH			
17.	U14EE018	KUNDAN KUMAR			
18.	U14EE019	LANKELA SRINIVASULU			
19.	U14EE020	LAYEEK ASHAD			
20.	U14EE021	MD INAM			
21.	U14EE022	MEHBOOB HUSSAIN			
22.	U14EE023	MUTHYALA HEMANTH SAI			
23.	U14EE024	NAINA MOHAMMED			
24.	U14EE025	KOYA NARESH.			
25.	U14EE026	N. VIJAYA SEKHAR			
26.	U14EE027	NITESH PRASAD			
27.	U14EE028	NITISH KUMAR			
28.	U14EE029	NOOR MOHAMED.M			
29.	U14EE030	PALAPARTHI TIRUMALA RAO			
30.	U14EE031	PINNAMANENI PRAVEEN KUMAR			
31.	U14EC077	ALURU MANIRATHNAM.			
32.	U14EC078	MANNEM MAHANTH REDDY			
33.	U14EC079	MANTU KUMAR SINGH			
34.	U14EC080	MARKA RAJ KUMAR			
35.	U14EC081	MAYANK HARSHIT			
36.	U14EC082	MD.FAIYAZ ALAM			
37.	U14EC083	MEENAAKSHI S			
38.	U14EC084	MELARGOAD KALATTAR RAKESH			
39.	U14EC086	MOHAMED KASHIFUDDIN.B			
40.	U14EC087	MOLABANTI SAI KARTHIK			

41.	U14EC088	VASIREDDY MOUNIKA.			
42.	U14EC089	MUDRAKOLLA SURESH SACHIN	Suresh	Suresh	Suresh
43.	U14EC090	MUTYALA SAI HARISHITHA			
44.	U14EC091	NADENDLA VANAJA	Vanaja	Vanaja	Vanaja
45.	U14EC092	NAGUNOORI SANKIRTH KUMAR			
46.	U14EC093	NALAMARU RAVALI	Ravali	Ravali	Ravali
47.	U14EC095	NARESH .I	Naresh	Naresh	Naresh
48.	U14EC096	MOGAL NASEER.			
49.	U14EC097	MATHEGAM NIHAL REDDY	Reddy	Reddy	Reddy
50.	U14EC098	NILKAMAL KUMAR			
51.	U14EC099	PADALA SUBRAHMANYAM			
52.	U14EC100	PALAPARTHI RAMBABU			
53.	U14EC101	PANDEM RAGHAVENDRA REDDY			
54.	U14EC102	PAPIJENNI RAMANAREDDY			
55.	U14EC103	PAPUGANI PARTHASARADHI.			
56.	U14EC104	PEDINEEDI VIJAYA BHARGAVI			
57.	U14EC105	PEDDISETTI VINAY	Vinay	Vinay	Vinay.
58.	U14EC106	PENGALAPATI BHARATHI			
59.	U14EC107	PILLI DANIEL PHILIP MOSES			
60.	U14EC108	PONNAGANTI MANOJ DEEP			
61.	U14EC109	G PRANAY KUMAR			
62.	U14EC110	PRASANNA.S	Prasanna	Prasanna	Prasanna
63.	U14EC111	GADDAM VENKATA RAVI PRASAD PRATHIMA			

Signature of HOD

Head of the Department
Department of E.E.E.
BHARATH INSTITUTE OF HIGHER EDUCATION & RESEARCH
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Chennai-600 073, INDIA.

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ABET

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE - Industrial Applications of PLC and SCADA

FEEDBACK FORM					Date: 20.12.17
Name	Annaram Bhasker Goud				
Register Number	~~8190889158~~ U14EE008				
Phone Number	8190889158				
Email address	bhaskergadprince@gmail.com				
	Poor	**Fair**	**Good**	**Very Good**	**Excellent**
Overall Program			✓		
Resource person			✓		
Audio visual aids & Technology used		✓			
Presentation Handouts			✓		

Student's Signature

Bharath
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BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY
No. 173, Agharam Road, Selaiyur, Chennai, T.N. - 600 073.

ABET

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE - Industrial Applications of PLC and SCADA

FEEDBACK FORM					Date: 20.12.17
Name	Palaparthy Thirumala Rao				
Register Number	U14 EE 030				
Phone Number	9087847757				
Email address	thirumalapalaparthi@gmail.com				
	Poor	**Fair**	**Good**	**Very Good**	**Excellent**
Overall Program				✓	
Resource person				✓	
Audio visual aids & Technology used					✓
Presentation Handouts				✓	

Palaparthy

Student's Signature

Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

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NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **ABHISHEK KUMAR (U14EE002)**

has attended Value added Course On ***"Industrial Applications of PLC and SCADA"*** organized by the School of Electrical Engineering, BIHER conducted from 18.12.2017 to 20.12.2017.

COURSE COORDINATOR

CONVENOR

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
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SCHOOL OF ELECTRICAL ENGINEERING

Industrial Applications of PLC and SCADA dated on *18.12.2017, 19.12.2017 and 20.12.2017* conducted by school of Electrical Engineering

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SCHOOL OF ELECTRICAL ENGINEERING

Innovations in processor Architecture

Value Added Course-2017

Course Objective

The objective of this course of study is to provide students with a glimpse into the semiconductor industry that has been the foundation upon which the electronics industry has been based for the past half century, and to provide insight into the future of that industry as well as nanotechnology in general. In the last 50 years, the dimensions of the features built into integrated circuits have shrunk from 25 mm to 25 nm. Over the next decade these features will approach atomic dimensions, giving rise to a host of unique nanotechnology challenges and opportunities.

The definition and description of the terminology and processes of microelectronics; semiconductor facilities and chemical processes for integrated circuit manufacture with an emphasis upon unit processes; the major unit processes including thin-film metal and dielectric deposition and etching, silicon oxidation and etching, ion implantation, diffusion, lithography, and planarization; an overview of promising nano patterning and nanofabrication techniques, such as electron and other particle-beam imaging, nanoimprint, and near-field probe imaging.

Resource Persons :

1.Ms.S.Saravana

2.Ms.K.Subbulakshmi

3.Ms.B.Hemalatha

Convener

Dr.M.Sangeetha

HOD/ECE

INSTITUTE OF HIGHER EDUCATION AND RESEARCH DEPT of ECE Chennai-73

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INSTITUTE OF HIGHER EDUCATION AND RESEARCH
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CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 01.11.2017

The course on Innovations in processor Architecture is planned by School of Electrical Engineering which commences on 27.11.2017(Monday). In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

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To,
Copy to ECE Department,
Copy to EEE Department,
Department Notice Board

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INSTITUTE OF HIGHER EDUCATION AND RESEARCH
Declared as Deemed-to-be-University u/s 3 of the UGC Act, 1956

SCHOOL OF ELECTRICAL ENGINEERING

Innovations in processor Architecture

SCHEDULE

Contact Hours : 32 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
27.11.2017	FN	9.00 am to 12.30 pm	Organization of the von Neumann machine; Instruction formats; Pipeline - fetch/execute cycle, Instruction decoding and execution; Registers and register files; Instruction types and addressing modes; Subroutine call and return mechanisms; Other design issues	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Data Representation, Hardware and software implementation of arithmetic unit for common arithmetic operations: addition, subtraction	Ms.S.Saravana
28.11.2017	FN	9.00 am to 12.30 pm	multiplication, division(Fixed point and floating point)-floating point IEEE standards	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Conversion between integer and real numbers- rounding and truncation; The generation of higher order functions from square roots to transcendental functions; Representation of non-numeric data (character codes, graphical data)	Ms.K.Subbulakshmi
29.11.2017	FN	9.00 am to 12.30 pm	Memory systems hierarchy; Coding, data compression, and data integrity;	Ms.S.Saravana

			Electronic, magnetic and optical technologies; Main memory organization, Types of Main memories, and its characteristics and performance;	
	AN	1.30 pm to 4 pm	Organization of the von Neumann machine; Instruction formats; Pipeline - fetch/execute cycle, Instruction decoding and execution; Registers and register files; Instruction types and addressing modes; Subroutine call and return mechanisms; Other design issues	Ms.K.Subbulakshmi
30.11.2017	FN	9.00 am to 12.30 pm	Latency, cycle time, bandwidth, and interleaving; Cachememories (address mapping, line size, replacement and write-back policies)	Ms.B.Hemalatha
	AN	1.30 pm to 5 pm	Virtual memory systems-paging, segmentation, address mapping, page tables, page replacement algorithms; Reliability of memory systems; error detecting and error correcting systems	Ms.S.Saravana
01.12.2017	FN	9.00 am to 12.30 pm	I/O fundamentals: handshaking, buffering; I/O techniques: programmed I/O, interrupt-driven I/O, DMA; Buses: bus protocols, local and geographic arbitration. Interrupt structures: vectored and prioritized, interrupt overhead, interrupts and reentrant code	Ms.K.Subbulakshmi
	AN	1.30 pm to 5 pm	External storage systems; organization and structure of disk drives and optical memory; Flashmemories, Basic I/O controllers such as a keyboard and a mouse;RAID architectures; I/O Performance; SMART technology and fault detection	Ms.S.Saravana

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Innovations in processor Architecture

List Of Participants

Date:27.11.2017

Sl.no	REG.NO	NAME OF THE CANDIDATE
1	U14EC001	AAKAASH THAKUR
2	U14EC004	K ABHILASH REDDY
3	U14EC006	ADDUGALA RAMA DEVI
4	U14EC007	ADHARSH.A .I
5	U14EC008	ADIREDDY PRAVEEN
6	U14EC010	AMARJEET KUMAR
7	U14EC016	ARCHANA.R
8	U14EC017	ASARA ANITH RAO
9	U14EC018	BANKIM CHANDRA BHARTI
10	U14EC019	BEDDINTI PRAVEEN KUMAR
11	U14EC022	BOYAPATI PUSHYAMITHRA
12	U14EC024	CHANDRALEKA.K
13	U14EC025	CHEKURI.VENKATA MAHESH
14	U14EC026	CHINTA ANVESH
15	U14EC028	DEBAJIT HAZARIKA
16	U14EC035	DUVVURU SREENIVASA TEJA
17	U14EC036	EJJAGIRI PRAVEEN
18	U14EC037	VIJAYA LAKSHMI EJJI

19	U14EC040	GARAGA SIVA SURYA DEEPAK
20	U14EC043	GOVINDUGARI NITHIN REDDY
21	U14EC044	GUJJARI SHIVADURGA PRASAD
22	U14EC045	GULAM AHMED REJA
23	U14EC050	KATHA HARSHA VARDHAN REDDY
24	U14EC052	JERALD.M.S
25	U14EC053	KAKARAPARTHY CHITRA HARSHAN
26	U14EC056	KALAI ARASI.M
27	U14EC058	KAMIREDDY SAI VEERA LAKHSMI MONIKA
28	U14EC059	KANALA RAMANJANEYA REDDY
29	U14EC070	KONDA MOHITH KUMAR REDDY
30	U14EC071	KONDURI SURENDRAREDDY
31	U14EC072	KONDURU PAVAN SAI
32	U14EC073	KOTA VIDYA SAGAR
33	U14EC078	MANNEM MAHANTH REDDY
34	U14EC080	MARKA RAJ KUMAR
35	U14EC082	MD.FAIYAZ ALAM
36	U14EC087	MOLABANTI SAI KARTHIK
37	U14EC088	VASIREDDY MOUNIKA.
38	U14EC089	MUDRAKOLLA SURESH SACHIN
39	U14EC090	MUTYALA SAI HARISHITHA
40	U14EC098	NILKAMAL KUMAR
41	U14EC099	PADALA SUBRAHMANYAM
42	U14EC100	PALAPARTHI RAMBABU
43	U14EC107	PILLI DANIEL PHILIP MOSES

44	U14EC108	PONNAGANTI MANOJ DEEP
45	U14EC109	G PRANAY KUMAR
46	U14EC116	KAKUMANU RADHA RANI
47	U14EC117	PAWAR.SUSHEEL KUMAR
48	U14EC139	SRIRAMULA PRANAV
49	U14EC140	SUSHEEL RANJAN
50	U14EC141	SWETHA HARIDASAN
51	U14EC148	THILLAI VANI.S
52	U14EC149	THIRUVATTURU HARIKRISHNA
53	U14EC158	VANGALA.CHANDRA SEKHAR REDDY
54	U14EC162	BUKAI VENKATESH NAIK.
55	U14EC165	VISWANATHAN.B
56	U14EC166	VONDANA TARAKESHWAR RAO

(Dr.M.Sangeetha)

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SCHOOL OF ELECTRICAL ENGINEERING

Course on **Innovations in processor Architecture** dated on 27.11.2017 conducted by School of Electrical Engineering

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INSTITUTE OF HIGHER EDUCATION AND RESEARCH

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NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **CHINTA ANVESH(U14EC026)** has attended Value added Course On ***"Innovations In Processor Architecture"*** organized by the School of Electrical Engineering, BIHER conducted from 27-11-2017 to 01-12-2017.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Innovations in processor Architecture

FEED BACK FORM				Date: 1/12/17	
Name	Kota Vidya Sagar.				
Register number	U14EC073				
Phone number	9123467781				
Email address	Vidya1234@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
TheSpeaker					✓
Audio,Visual Aids Technology used		✓		✓	
Presentation hand outs					✓

Kotha Vidya Sagar.

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Innovations in processor Architecture

FEED BACK FORM					Date: 1/12/17
Name	Lins Rexine D				
Register number	U14EE704				
Phone number	7358179091				
Email address	Lins2010@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
TheSpeaker					✓
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

Value Added Courses (2017 -2018)

Introduction to Wireless Sensor Networks

Course Objective

Wireless sensor networks (WSNs) emerge as an active research area in which challenging topics involve energy consumption, routing algorithms, selection of sensors location according to a given premise, robustness, efficiency, and so forth. Despite the open problems in WSNs, there are already a high number of applications available. In all cases for the design of any application, one of the main objectives is to keep the WSN alive and functional as long as possible. A key factor in this is the way the network is formed. The course is focused on whether a single or multiple sinks are employed, nodes are static or mobile, the formation is event detection based or not, and network backbone is formed or not. We focus on recent works and present a discussion of their advantages and drawbacks.

Resource Persons :

1.Dr.S.Arulselvi

2.Dr.B.Karthik

3.Ms.M.Jasmin

Convener

Dr.M.Sangeetha

HOD/ECE

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SCHOOL OF ELECTRICAL ENGINEERING

Date: 01.09.2017

The course on Introduction to wireless Sensor Networks is planned by School of Electrical Engineering which commences on 26-9-2017(Tuesday).In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

BHARATH INSTITUTE OF HIGHER EDUCATION AND RESEARCH
DEPT of ECE
Chennai-73

To,
Copy to ECE Department,
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SCHOOL OF ELECTRICAL ENGINEERING

Course on Application of Sensor networks

SCHEDULE

Contact Hours : 32 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
26-9-2017	FN	9.00 am to 12.30 pm	Characteristic requirements for WSN - Challenges for WSNs – WSN vs Adhoc Networks - Sensor node architecture – Commercially available sensor nodes – Imote, IRIS, Mica Mote, EYES nodes, BT nodes, Telos B, Sunspot	Dr.S.Arulselvi
	AN	1.30 pm to 4 pm	Physical layer and transceiver design considerations in WSNs, Energy usage profile, Choice of modulation scheme, Dynamic modulation scaling, Antenna considerations.	Dr.B.Karthik
27-9-2017	FN	9.00 am to 12.30 pm	Fundamentals of MAC protocols - Low duty cycle protocols and wakeup concepts – Contention based protocols - Schedule-based protocols	Dr.S.Arulselvi
	AN	1.30 pm to 4 pm	SMAC - BMAC - Traffic-adaptive medium access protocol (TRAMA) - The IEEE 802.15.4 MAC protocol	Ms.M.Jasmin
28-9-2017	FN	9.00 am to 12.30 pm	Routing Challenges and Design Issues in Wireless Sensor Networks, Flooding and gossiping – Data centric Routing – SPIN – Directed Diffusion – Energy aware routing - Gradient-based routing - Rumor Routing – COUGAR – ACQUIRE – Hierarchical Routing - LEACH, PEGASIS – Location Based Routing	Dr.B.Karthik
	AN	1.30 pm to 4 pm	GF, GAF, GEAR, GPSR – Real Time routing Protocols – TEEN, APTEEN, SPEED, RAP -	Ms.M.Jasmin

			Data aggregation - data aggregation operations - Aggregate Queries in Sensor Networks - Aggregation Techniques – TAG, Tiny DB	
29-9-2017	FN	9.00 am to 12.30 pm	Operating Systems for Wireless Sensor Networks – Introduction - Operating System Design Issues - Examples of Operating Systems – Tiny OS – Mate – Magnet OS – MANTIS - OSPM - EYES OS	Dr.S.Arulselvi
	AN	1.30 pm to 5 pm	SenOS – EMERALDS – PicOS – Introduction to Tiny OS – NesC – Interfaces and Modules- Configurations and Wiring - Generic Components - Programming in Tiny OS using NesC, Emulator TOSSIM.	Dr.B.Karthik
30-9-2017	FN	9.00 am to 12.30 pm	WSN Applications - Home Control - Building Automation - Industrial Automation - Medical Applications - Reconfigurable Sensor Networks - Highway Monitoring - Military Applications - Civil and Environmental Engineering Applications	Ms.M.Jasmin
	AN	1.30 pm to 5 pm	Wildfire Instrumentation - Habitat Monitoring - Nanoscopic Sensor Applications – Case Study: IEEE 802.15.4 LR-WPANs Standard - Target detection and tracking - Contour/edge detection - Field sampling.	Dr.B.Karthik

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Course on Application of Sensor networks

List Of Participants

Date:26.09.2017

Sl.No	REG.N0	NAME OF THE CANDIDATE
1	U15EC002	AKHIL CHELLUBOINA
2	U15EC003	AKULA SUJITH KRISHNA
3	U15EC004	ALOK KUMAR
4	U15EC005	ALUVALA ARUN KUMAR GOUD
5	U15EC006	AMAYA E
6	U15EC007	AMBULA DEVI GOWTHAM
7	U15EC009	AMMISETTI AVINASH
8	U15EC010	ANKIT KUMAR DUBEY
9	U15EC015	ATTAR MOHAMMED TOUSIF
10	U15EC016	ATUKURI AVINASSH
11	U15EC017	BASETTY HIMABINDU
12	U15EC018	BOJJA PHANINDHRA REDDY
13	U15EC019	C. SHIVARAMAN SRIKANTH
14	U15EC020	CHANDAN PANDAY
15	U15EC021	CHAPARTHI KARTHIK
16	U15EC022	CHEKKA KESAVA PRAJWAL
17	U15EC023	CHITTIBOMMA SWATHI
18	U15EC024	DASARI HARI SAI KUMAR
19	U15EC025	DUDEKULA FAYAZ
20	U15EC026	DUDEKULA NOORNIYAZ
21	U15EC027	DUGYALA PREETHI
22	U15EC028	FAHIMA NASREEN S

23	U15EC030	GADE MOUNIKA
24	U15EC032	GORANTLA SRINADH
25	U15EC034	GUDIVADA HEMASAGAR
26	U15EC035	GUNDRATHI AJAY KUMAR
27	U15EC039	J PHANEENDRANATH
28	U15EC040	JAGALURU THIMMA REDDY
29	U15EC041	JAGARLAMUDI CHAITANYA
30	U15EC042	JAKKU MANIDEEP
31	U15EC044	JETTY SAI SUDHEER
32	U15EC046	JONNALAGADDA VENKATA MANOJ KUMAR
33	U15EC047	K O HARICHANDANA
34	U15EC050	KARICHETI BALAKRISHNA
35	U15EC091	NAMBURI VENKATA ANUSHA

(Dr.M.Sangeetha)

HOD/ECE

DEPT of ECE

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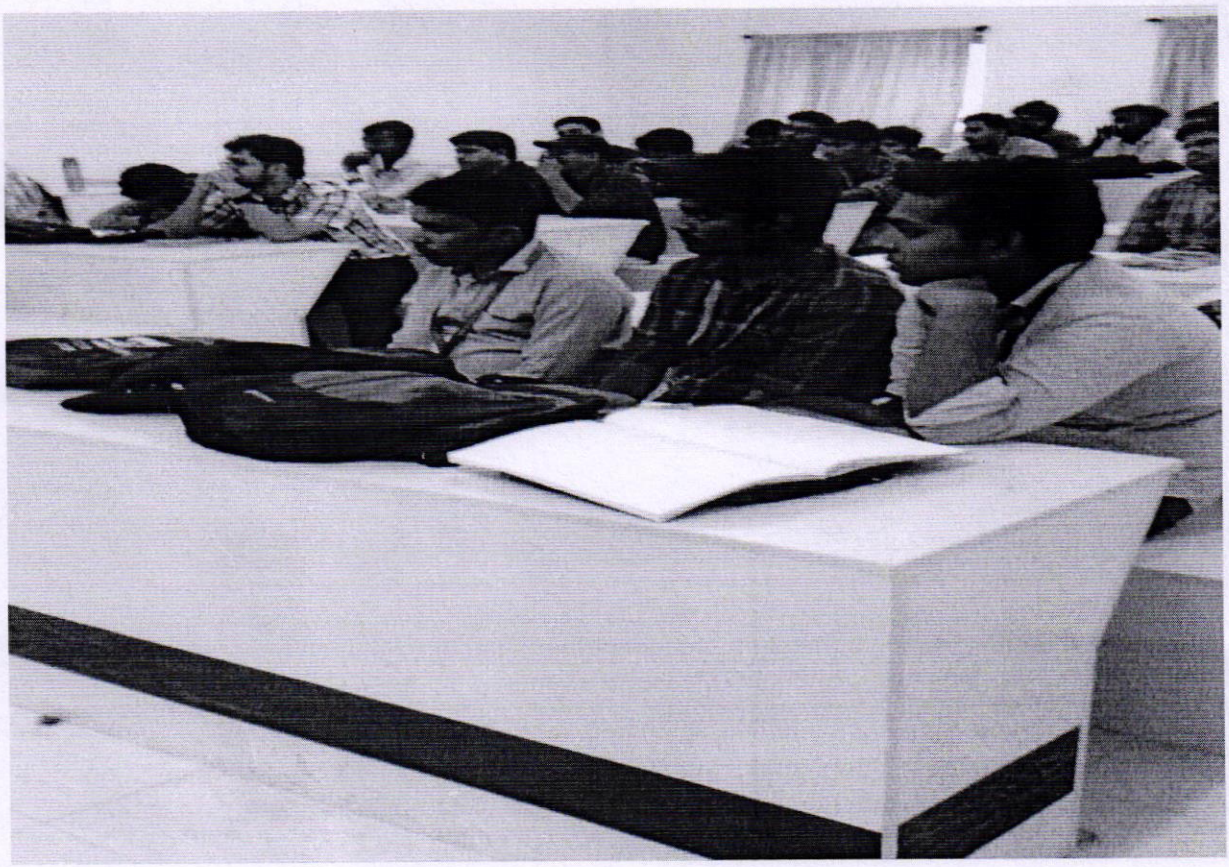
SCHOOL OF ELECTRICAL ENGINEERING

Course on Introduction toWireless sensor Networks dated on 26.09.2017 conducted by school of Electrical Engineering



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NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **NAMBURI VENKATA SESHA ANUSHA(U15EC091**

has attended Value added Course On ***"Introduction to Wireless Sensor Networks"*** organized by the School of Electrical Engineering, BIHER conducted from 26-09-2017 to 30-09-2017.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Introduction to wireless Sensor Networks

FEED BACK FORM					Date: 30.09.2014
Name	U15EE026 / Prashant Kumar pandey				
Register number	U15EE026				
Phone number	8136415209				
Email address	Prashantbabe@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	✓
TheSpeaker				✓	
Audio,Visual Aids Technology used				✓	
Presentation hand outs			✓		✓

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Introduction to wireless Sensor Networks

FEED BACK FORM					Date: 30.09.2017
Name	Jakku manideep				
Register number	U15EC042.				
Phone number	7342678901				
Email address	Jakku123@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker				✓	
Audio,Visual Aids Technology used				✓	
Presentation hand outs			✓		

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

Value Added Courses (2017 -2018)

Course on Application of Sensor networks

Course Objective

Wireless sensor networks (WSNs) emerge as an active research area in which challenging topics involve energy consumption, routing algorithms, selection of sensors location according to a given premise, robustness, efficiency, and so forth. Despite the open problems in WSNs, there are already a high number of applications available. In all cases for the design of any application, one of the main objectives is to keep the WSN alive and functional as long as possible. A key factor in this is the way the network is formed. The course is focused on whether a single or multiple sinks are employed, nodes are static or mobile, the formation is event detection based or not, and network backbone is formed or not. We focus on recent works and present a discussion of their advantages and drawbacks.

Resource Persons :

1.Dr.S.Arulselvi

2.Dr.B.Karthik

3.Ms.M.Jasmin

Convener

Dr.M.Sangeetha

HOD/ECE

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CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 02.08.2017

The course on Application of Sensor Networks is planned by School of Electrical Engineering which commences on 28-8-2017(Monday).In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

To,
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Copy to EEE Department,
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SCHOOL OF ELECTRICAL ENGINEERING

Course on Application of Sensor networks

SCHEDULE

Contact Hours : 32 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
28-8-2017	FN	9.00 am to 12.30 pm	Characteristic requirements for WSN - Challenges for WSNs – WSN vs Adhoc Networks - Sensor node architecture – Commercially available sensor nodes – Imote, IRIS, Mica Mote, EYES nodes, BT nodes, Telos B, Sunspot	Dr.S.Arulselvi
	AN	1.30 pm to 4 pm	Physical layer and transceiver design considerations in WSNs, Energy usage profile, Choice of modulation scheme, Dynamic modulation scaling, Antenna considerations.	Dr.B.Karthik
29-8-2017	FN	9.00 am to 12.30 pm	Fundamentals of MAC protocols - Low duty cycle protocols and wakeup concepts – Contention based protocols - Schedule-based protocols	Dr.S.Arulselvi
	AN	1.30 pm to 4 pm	SMAC - BMAC - Traffic-adaptive medium access protocol (TRAMA) - The IEEE 802.15.4 MAC protocol	Ms.M.Jasmin
30-8-2017	FN	9.00 am to 12.30 pm	Routing Challenges and Design Issues in Wireless Sensor Networks, Flooding and gossiping – Data centric Routing – SPIN – Directed Diffusion – Energy aware routing - Gradient-based routing - Rumor Routing – COUGAR – ACQUIRE – Hierarchical Routing - LEACH, PEGASIS – Location Based Routing	Dr.B.Karthik
	AN	1.30 pm to 4 pm	GF, GAF, GEAR, GPSR – Real Time routing Protocols – TEEN, APTEEN, SPEED, RAP -	Ms.M.Jasmin

			Data aggregation - data aggregation operations - Aggregate Queries in Sensor Networks - Aggregation Techniques – TAG, Tiny DB	
31-8-2017	FN	9.00 am to 12.30 pm	Operating Systems for Wireless Sensor Networks – Introduction - Operating System Design Issues - Examples of Operating Systems – Tiny OS – Mate – Magnet OS – MANTIS - OSPM - EYES OS	Dr.S.Arulselvi
	AN	1.30 pm to 5 pm	SenOS – EMERALDS – PicOS – Introduction to Tiny OS – NesC – Interfaces and Modules- Configurations and Wiring - Generic Components - Programming in Tiny OS using NesC, Emulator TOSSIM.	Dr.B.Karthik
1-9-2017	FN	9.00 am to 12.30 pm	WSN Applications - Home Control - Building Automation - Industrial Automation - Medical Applications - Reconfigurable Sensor Networks - Highway Monitoring - Military Applications - Civil and Environmental Engineering Applications	Ms.M.Jasmin
	AN	1.30 pm to 5 pm	Wildfire Instrumentation - Habitat Monitoring - Nanoscopic Sensor Applications – Case Study: IEEE 802.15.4 LR-WPANs Standard - Target detection and tracking - Contour/edge detection - Field sampling.	Dr.B.Karthik

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Course on Application of Sensor networks

List Of Participants

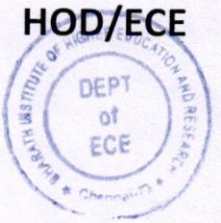
Date:28.08.2017

Sl.No	REG.N0	NAME OF THE CANDIDATE
1	U14EC002	AARTHI.P
2	U14EC003	ABBISETTY SAI NIHARIKA
3	U14EC007	ADHARSH.A .I
4	U14EC011	R AMULYA
5	U14EC016	ARCHANA.R
6	U14EC023	CHALUVADI DIVYA BHARATHI
7	U14EC024	CHANDRALEKA.K
8	U14EC032	DESHI VENKATESH
9	U14EC060	KANIKE SAIPRAKASH
10	U14EC061	KANNA SHIVA KRISHNA
11	U14EC069	KONAIAHGARI NAGA VAMSI KRISHNA
12	U14EC070	KONDA MOHITH KUMAR REDDY
13	U14EC077	ALURU MANIRATHNAM.
14	U14EC079	MANTU KUMAR SINGH
15	U14EC087	MOLABANTI SAI KARTHIK
16	U14EC089	MUDRAKOLLA SURESH SACHIN
17	U14EC096	MOGAL NASEER.
18	U14EC103	PAPUGANI PARTHASARADHI.
19	U14EC105	PEDDISETTI VINAY
20	U14EC108	PONNAGANTI MANOJ DEEP
21	U14EC111	GADDAM VENKATA RAVI PRASAD PRATHIMA
22	U14EC113	PUNUGOTI ANUSHA
23	U14EC119	RACHAPALLI SAI MOHAN
24	U14EC123	CHEEDELLA SARACCHANDRA.
25	U14EC130	P SHOPIC
26	U14EC131	SINGAMREDDY MUKUNDESWAR REDDY
27	U14EC137	SRILADAGUDAM VANGATE SHALINI
28	U14EC139	SRIRAMULA PRANAV
29	U14EC144	SYED NAZIM PASHA KHADRI

30	U14EC103	PAPUGANI PARTHASARADHI.
31	U14EC146	TAMIL SELVI .K
32	U14EC151	THOODI SHEKAR REDDY
33	U14EE024	NAINA MOHAMMED
34	U14EE040	K RESHEENDAR
35	U14EE054	YOGESHWARAN .D

(Dr.M.Sangeetha)

HOD/ECE



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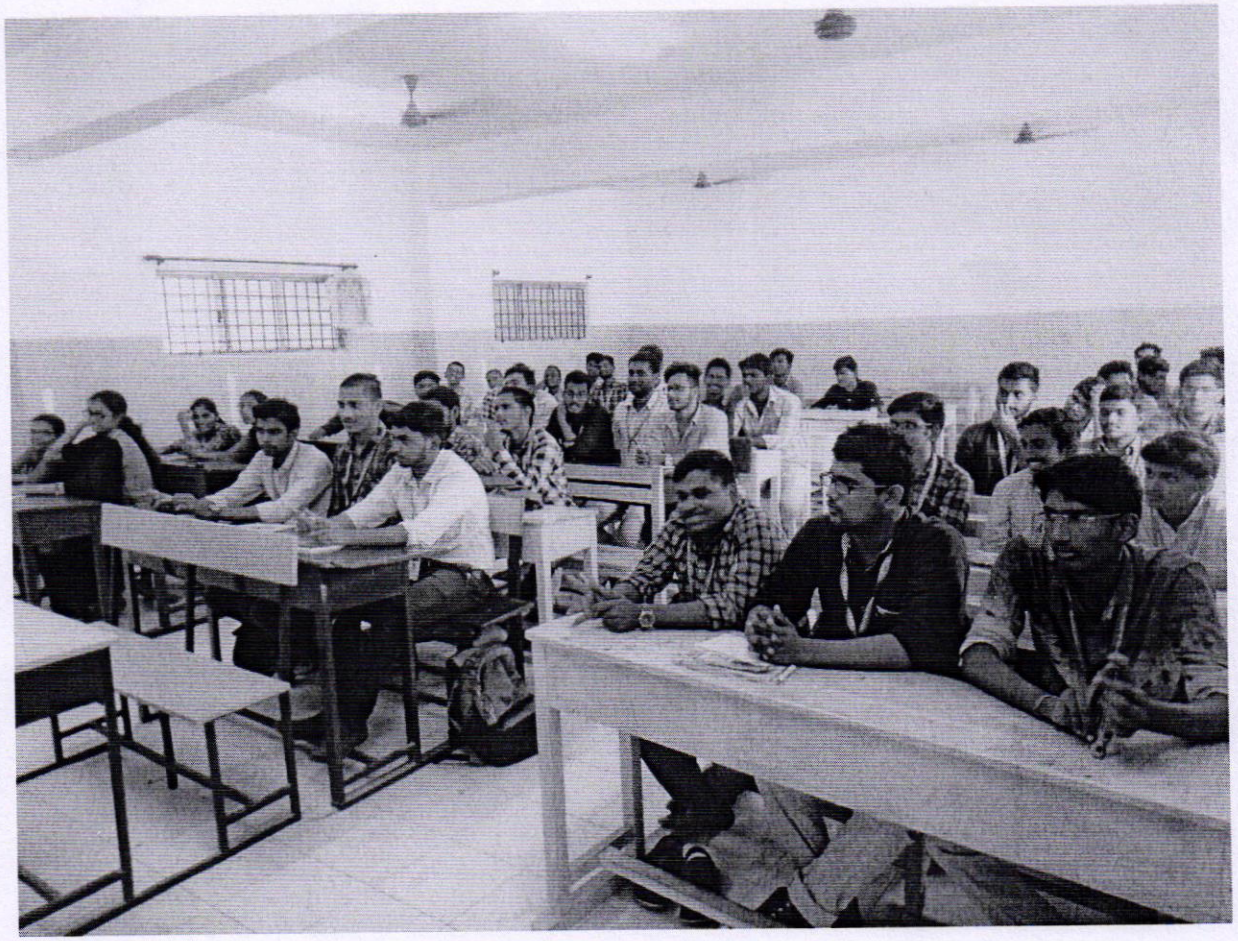
SCHOOL OF ELECTRICAL ENGINEERING

Course on Application of sensor Networks dated on 28.08.2017 conducted by school of Electrical Engineering



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NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **KANIKE SAIPRAKASH (U14EC060)** has attended Value added Course On **"*Application of Sensor Networks*"** organized by the School of Electrical Engineering, BIHER conducted from 28-08-2017 to 01-09-2017.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Course on Application of Sensor Networks

FEED BACK FORM					Date: 1/9/17
Name	P. Shopic				
Register number	U14EC130				
Phone number	9176416390				
Email address	Shopicdude@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker				✓	
Audio,Visual Aids Technology used					✓
Presentation hand outs					✓

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Course on Application of Sensor Networks

FEED BACK FORM					Date: 1/9/17
Name	K. Resheendar				
Register number	V14EE040				
Phone number	7358147091				
Email address	resheen3440@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker				✓	
Audio,Visual Aids Technology used					✓
Presentation hand outs					✓

Student Signature

Bharath
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SCHOOL OF ELECTRICAL ENGINEERING

Innovations in processor Architecture

Value Added Course-2017

Course Objective

The objective of this course of study is to provide students with a glimpse into the semiconductor industry that has been the foundation upon which the electronics industry has been based for the past half century, and to provide insight into the future of that industry as well as nanotechnology in general. In the last 50 years, the dimensions of the features built into integrated circuits have shrunk from 25 mm to 25 nm. Over the next decade these features will approach atomic dimensions, giving rise to a host of unique nanotechnology challenges and opportunities.

The definition and description of the terminology and processes of microelectronics; semiconductor facilities and chemical processes for integrated circuit manufacture with an emphasis upon unit processes; the major unit processes including thin-film metal and dielectric deposition and etching, silicon oxidation and etching, ion implantation, diffusion, lithography, and planarization; an overview of promising nano patterning and nanofabrication techniques, such as electron and other particle-beam imaging, nanoimprint, and near-field probe imaging.

Resource Persons :

1.Ms.S.Saravana

2.Ms.K.Subbulakshmi

3.Ms.B.Hemalatha

Convener

Dr.M.Sangeetha

HOD/ECE

INSTITUTE OF HIGHER EDUCATION AND RESEARCH DEPT of ECE Chennai-73

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CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 01.11.2017

The course on Innovations in processor Architecture is planned by School of Electrical Engineering which commences on 27.11.2017(Monday). In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

DEPT of ECE

To,
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Copy to EEE Department,
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SCHOOL OF ELECTRICAL ENGINEERING

Innovations in processor Architecture

SCHEDULE

Contact Hours : 32 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
27.11.2017	FN	9.00 am to 12.30 pm	Organization of the von Neumann machine; Instruction formats; Pipeline - fetch/execute cycle, Instruction decoding and execution; Registers and register files; Instruction types and addressing modes; Subroutine call and return mechanisms; Other design issues	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Data Representation, Hardware and software implementation of arithmetic unit for common arithmetic operations: addition, subtraction	Ms.S.Saravana
28.11.2017	FN	9.00 am to 12.30 pm	multiplication, division(Fixed point and floating point)-floating point IEEE standards	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Conversion between integer and real numbers- rounding and truncation; The generation of higher order functions from square roots to transcendental functions; Representation of non-numeric data (character codes, graphical data)	Ms.K.Subbulakshmi
29.11.2017	FN	9.00 am to 12.30 pm	Memory systems hierarchy; Coding, data compression, and data integrity;	Ms.S.Saravana

			Electronic, magnetic and optical technologies; Main memory organization, Types of Main memories, and its characteristics and performance;	
	AN	1.30 pm to 4 pm	Organization of the von Neumann machine; Instruction formats; Pipeline - fetch/execute cycle, Instruction decoding and execution; Registers and register files; Instruction types and addressing modes; Subroutine call and return mechanisms; Other design issues	Ms.K.Subbulakshmi
30.11.2017	FN	9.00 am to 12.30 pm	Latency, cycle time, bandwidth, and interleaving; Cachememories (address mapping, line size, replacement and write-back policies)	Ms.B.Hemalatha
	AN	1.30 pm to 5 pm	Virtual memory systems-paging, segmentation, address mapping, page tables, page replacement algorithms; Reliability of memory systems; error detecting and error correcting systems	Ms.S.Saravana
01.12.2017	FN	9.00 am to 12.30 pm	I/O fundamentals: handshaking, buffering; I/O techniques: programmed I/O, interrupt-driven I/O, DMA; Buses: bus protocols, local and geographic arbitration. Interrupt structures: vectored and prioritized, interrupt overhead, interrupts and reentrant code	Ms.K.Subbulakshmi
	AN	1.30 pm to 5 pm	External storage systems; organization and structure of disk drives and optical memory; Flashmemories, Basic I/O controllers such as a keyboard and a mouse;RAID architectures; I/O Performance; SMART technology and fault detection	Ms.S.Saravana

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Innovations in processor Architecture

List Of Participants

Date:27.11.2017

Sl.no	REG.NO	NAME OF THE CANDIDATE
1	U14EC001	AAKAASH THAKUR
2	U14EC004	K ABHILASH REDDY
3	U14EC006	ADDUGALA RAMA DEVI
4	U14EC007	ADHARSH.A .I
5	U14EC008	ADIREDDY PRAVEEN
6	U14EC010	AMARJEET KUMAR
7	U14EC016	ARCHANA.R
8	U14EC017	ASARA ANITH RAO
9	U14EC018	BANKIM CHANDRA BHARTI
10	U14EC019	BEDDINTI PRAVEEN KUMAR
11	U14EC022	BOYAPATI PUSHYAMITHRA
12	U14EC024	CHANDRALEKA.K
13	U14EC025	CHEKURI.VENKATA MAHESH
14	U14EC026	CHINTA ANVESH
15	U14EC028	DEBAJIT HAZARIKA
16	U14EC035	DUVVURU SREENIVASA TEJA
17	U14EC036	EJJAGIRI PRAVEEN
18	U14EC037	VIJAYA LAKSHMI EJJI

19	U14EC040	GARAGA SIVA SURYA DEEPAK
20	U14EC043	GOVINDUGARI NITHIN REDDY
21	U14EC044	GUJJARI SHIVADURGA PRASAD
22	U14EC045	GULAM AHMED REJA
23	U14EC050	KATHA HARSHA VARDHAN REDDY
24	U14EC052	JERALD.M.S
25	U14EC053	KAKARAPARTHY CHITRA HARSHAN
26	U14EC056	KALAI ARASI.M
27	U14EC058	KAMIREDDY SAI VEERA LAKHSMI MONIKA
28	U14EC059	KANALA RAMANJANEYA REDDY
29	U14EC070	KONDA MOHITH KUMAR REDDY
30	U14EC071	KONDURI SURENDRAREDDY
31	U14EC072	KONDURU PAVAN SAI
32	U14EC073	KOTA VIDYA SAGAR
33	U14EC078	MANNEM MAHANTH REDDY
34	U14EC080	MARKA RAJ KUMAR
35	U14EC082	MD.FAIYAZ ALAM
36	U14EC087	MOLABANTI SAI KARTHIK
37	U14EC088	VASIREDDY MOUNIKA.
38	U14EC089	MUDRAKOLLA SURESH SACHIN
39	U14EC090	MUTYALA SAI HARISHITHA
40	U14EC098	NILKAMAL KUMAR
41	U14EC099	PADALA SUBRAHMANYAM
42	U14EC100	PALAPARTHI RAMBABU
43	U14EC107	PILLI DANIEL PHILIP MOSES

44	U14EC108	PONNAGANTI MANOJ DEEP
45	U14EC109	G PRANAY KUMAR
46	U14EC116	KAKUMANU RADHA RANI
47	U14EC117	PAWAR.SUSHEEL KUMAR
48	U14EC139	SRIRAMULA PRANAV
49	U14EC140	SUSHEEL RANJAN
50	U14EC141	SWETHA HARIDASAN
51	U14EC148	THILLAI VANI.S
52	U14EC149	THIRUVATTURU HARIKRISHNA
53	U14EC158	VANGALA.CHANDRA SEKHAR REDDY
54	U14EC162	BUKAI VENKATESH NAIK.
55	U14EC165	VISWANATHAN.B
56	U14EC166	VONDANA TARAKESHWAR RAO

(Dr.M.Sangeetha)

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SCHOOL OF ELECTRICAL ENGINEERING

Course on **Innovations in processor Architecture** dated on 27.11.2017 conducted by School of Electrical Engineering

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ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **CHINTA ANVESH(U14EC026)** has attended Value added Course On ***"Innovations In Processor Architecture"*** organized by the School of Electrical Engineering, BIHER conducted from 27-11-2017 to 01-12-2017.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

Bharath
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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Innovations in processor Architecture

FEED BACK FORM				Date: 1/12/17	
Name	Kota Vidya Sagar.				
Register number	U14EC073				
Phone number	9123467781				
Email address	Vidya1234@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
TheSpeaker					✓
Audio,Visual Aids Technology used		✓		✓	
Presentation hand outs					✓

Kotha Vidya Sagar.

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Innovations in processor Architecture

FEED BACK FORM					Date: 1/12/17
Name	Lins Rexine D				
Register number	U14EE704				
Phone number	7358179091				
Email address	Lins2010@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	
TheSpeaker					✓
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

Value Added Courses (2017 -2018)

Familiarization of Matlab and simulation

Course Objective

MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming environment. Furthermore, MATLAB is a modern programming language environment ,it has sophisticated data structures, contains built-in editing and debugging tools, and supports object-oriented programming. These factors make MATLAB an excellent tool for teaching and research. MATLAB has many advantages compared to conventional computer languages (e.g., C, FORTRAN) for solving technical problems. MATLAB is an interactive system whose basic data element is an array that does not require dimensioning. The software package has been commercially available since 1984 and is now considered as a standard tool at most universities and industries worldwide.

Resource Persons :

1.Ms.M.Jasmin

2.Ms.B.Hemalatha

3.Ms.S.Philomina

Convener

Dr.M.Sangeetha

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SCHOOL OF ELECTRICAL ENGINEERING

Date: 20.11.2017

The course on Familiarization of Matlab and simulation is planned by School of Electrical Engineering which commences on 18-12-17 (Monday). In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

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SCHOOL OF ELECTRICAL ENGINEERING

Familiarization of Matlab and simulation

SCHEDULE

Contact Hours : 31 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
18-12-2017	FN	9.00 am to 12.30 pm	Introduction to MATLAB Software MATLAB window Command window Workspace Command history Setting directory Working with the MATLAB user interface	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Character and string Arrays and vectors Column vectors Row vectors	Ms.S.Philomina
19-12-2017	FN	9.00 am to 12.30 pm	BODMAS Rules Arithmetic operations Operators and special characters Mathematical and logical operators Solving arithmetic equations	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Crating rows and columns Matrix Matrix operations Finding transpose, determinant and inverse Solving matrix	Ms.M.Jasmin
20-12-2017	FN	9.00 am to 12.30 pm	Trigonometric functions Complex numbers fractions Real numbers Complex numbers	Ms.S.Philomina
	AN	1.30 pm to 4 pm	Working with script tools Writing Script file	Ms.M.Jasmin

			Executing script files The MATLAB Editor Saving m files	
21-12-2017	FN	9.00 am to 12.30 pm	Plotting vector and matrix data Plot labelling, curve labelling and editing	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Basic Plotting Functions Creating a Plot Plotting Multiple Data Sets in One Graph Specifying Line Styles and Colors Graphing Imaginary and Complex Data Figure Windows Displaying Multiple Plots in One Figure	Ms.S.Philomina
22-12-2017	FN	9.00 am to 12.30 pm	Creating Mesh and Surface About Mesh and Surface Visualizing Subplots	Ms.M.Jasmin
	AN	1.30 pm to 5 pm	Introduction Of Simulink Simulink Environment & Interface Study of Library Circuit Oriented Design Equation Oriented Design	Ms.S.Philomina

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Familiarization of Matlab and simulation

List Of Participants

Date:18.12.2017

Sl.no	REG.NO	NAME OF THE CANDIDATE
1	U14EC001	AAKAASH THAKUR
2	U14EC003	ABBISETTY SAI NIHARIKA
3	U14EC010	AMARJEET KUMAR
4	U14EC013	ANKIT KAUSHAL
5	U14EC016	ARCHANA.R
6	U14EC017	ASARA ANITH RAO
7	U14EC018	BANKIM CHANDRA BHARTI
8	U14EC019	BEDDINTI PRAVEEN KUMAR
9	U14EC020	BETHALA MOURYA
10	U14EC025	CHEKURI.VENKATA MAHESH
11	U14EC026	CHINTA ANVESH
12	U14EC033	N DHEERAJ
13	U14EC034	DOLLY NISHA J.S.
14	U14EC040	GARAGA SIVA SURYA DEEPAK
15	U14EC041	S GOKUL
16	U14EC042	GOURU VENKATA SAI PRAKASH
17	U14EC066	MANAM KOKILA.
18	U14EC067	KOMMANI DIVYA SREE

19	U14EC068	KOMMIDI PUNNAM CHANDER
20	U14EC072	KONDURU PAVAN SAI
21	U14EC073	KOTA VIDYA SAGAR
22	U14EC075	SINGAMALA MALLIKARJUNA REDDY
23	U14EC081	MAYANK HARSHIT
24	U14EC082	MD.FAIYAZ ALAM
25	U14EC083	MEENAAKSHI S
26	U14EC089	MUDRAKOLLA SURESH SACHIN
27	U14EC090	MUTYALA SAI HARISHITHA
28	U14EC103	PAPUGANI PARTHASARADHI.
29	U14EC104	PEDINEEDI VIJAYA BHARGAVI
30	U14EC106	PENGALAPATI BHARATHI
31	U14EC113	PUNUGOTI ANUSHA
32	U14EC114	RACHAMADUGU MANISH
33	U14EC121	SALUMURI RAVI TEJA
34	U14EC123	CHEEDELLA SARACCHANDRA.
35	U14EC128	SHAIK.ALEEM

(Dr.M.Sangeetha)

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SCHOOL OF ELECTRICAL ENGINEERING

Course on **Familiarization of Matlab and simulation** dated on 18.12.2017 conducted by School of Electrical Engineering

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INSTITUTE OF HIGHER EDUCATION AND RESEARCH

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ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **ANKIT KAUSHAL(U14EC013)**

has attended Value added Course On "***Familiarization of Matlab and Simulation***" organized by the School of Electrical Engineering, BIHER conducted from 18-12-2017 to 22-12-2017.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Familiarization of Matlab and simulation

FEED BACK FORM					Date: 22/12/17
Name	Nitish Kumar				
Register number	U14EE028				
Phone number	7358123 09				
Email address	nitish2009@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker			✓	✓	
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Familiarization of Matlab and simulation

FEED BACK FORM					Date: 22/12/17
Name	Meenakshi.S				
Register number	U14EC083				
Phone number	8844655 93				
Email address	meenumathi@gmail.com				
	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker			✓		
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Meen
Student Signature

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SCHOOL OF ELECTRICAL ENGINEERING

Fundamentals of Micro and NanoFabrication

Value Added Courses-2018

Course Objective

The objective of this course of study is to provide students with a glimpse into the semiconductor industry that has been the foundation upon which the electronics industry has been based for the past half century, and to provide insight into the future of that industry as well as nanotechnology in general. In the last 50 years, the dimensions of the features built into integrated circuits have shrunk from 25 mm to 25 nm. Over the next decade these features will approach atomic dimensions, giving rise to a host of unique nanotechnology challenges and opportunities.

The definition and description of the terminology and processes of microelectronics; semiconductor facilities and chemical processes for integrated circuit manufacture with an emphasis upon unit processes; the major unit processes including thin-film metal and dielectric deposition and etching, silicon oxidation and etching, ion implantation, diffusion, lithography, and planarization; an overview of promising nano patterning and nanofabrication techniques, such as electron and other particle-beam imaging, nanoimprint, and near-field probe imaging.

Resource Persons :

1.Ms.S.Saravana

2.Ms.K.Subbulakshmi

3.Ms.B.Hemalatha

Convener

Dr.M.Sangeetha

HOD/ECE

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CIRCULAR

SCHOOL OF ELECTRICAL ENGINEERING

Date: 2.02.2018

The course on Fundamentals of Micro and Nano Fabrication is planned by School of Electrical Engineering which commences on 01.03.2018(Wednesday).In this regard the students are requested to give their willingness to Course Coordinator. It is instructed to actively participate and get benefitted for the certified course.

Course Coordinator: M. Sowmiya Manoj
Contact No:7358747803
Email id : sowmiyamanoj.ece@bharathuniv.ac.in

(Dr.M.Sangeetha)
HOD/ECE

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SCHOOL OF ELECTRICAL ENGINEERING

Fundamentals of Micro and NanoFabrication

SCHEDULE

Contact Hours : 31 hrs

DATE	SESSION	Contact Hours	TOPICS	Resource person
01.03.2018	FN	9.00 am to 12.30 pm	Tunnel junction and applications of tunneling, Tunneling Through a Potential Barrier, Metal—Insulator, Metal-Semiconductor, and Metal-Insulator-Metal Junctions, Coulomb Blockade, Tunnel Junctions	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Tunnel Junction Excited by a Current Source. Spintronics and Foundations of nano-photonics.	Ms.S.Saravana
02.03.2018	FN	9.00 am to 12.30 pm	Field Emission, Gate—Oxide Tunneling and Hot Electron Effects in nano MOSFETs, Theory of Scanning Tunneling Microscope, Double Barrier Tunneling and the Resonant Tunneling Diode.	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Introduction to lithography- Contact, proximity printing and Projection Printing, Resolution Enhancement techniques, overlay-accuracies, Mask-Error enhancement factor (MEEF), Positive and negative photoresists, Electron Lithography, Projection Printing, Direct	Ms.K.Subbulakshmi

			writing,	
03.03.2018	FN	9.00 am to 12.30 pm	Electron resists. Lithography based on Surface Instabilities: Wetting, De-wetting, Adhesion, Limitations, Resolution and Achievable / line widths etc. Lift off process, Bulk Micro machining.	Ms.S.Saravana
	AN	1.30 pm to 4 pm	Introduction to MEMS and NEMS, working principles, as micro sensors (acoustic wave sensor, biomedical and biosensor, chemical sensor, optical sensor, capacitive sensor, pressure sensor and thermal sensor), micro actuation (thermal actuation, piezoelectric actuation and electrostatic actuation–micro gripers, motors, valves, pumps, accelerometers	Ms.K.Subbulakshmi
06.03.2018	FN	9.00 am to 12.30 pm	fluidics and capillary electrophoresis, active and passive micro fluidic devices, Pizoresistivity,Pizoelectricity and thermoelectricity, MEMS/NEMS design, processing, Oxidation, Sputter deposition, Evaporation, Chemical vapor deposition etc.	Ms.B.Hemalatha
	AN	1.30 pm to 4 pm	Introduction – Scaling of physical systems – Geometric scaling & Electrical system scaling.	Ms.S.Saravana
07.03.2018	FN	9.00 am to 12.30 pm	The Single-Electron Transistor: The Single- Electron Transistor Single-Electron Transistor Logic, Other SET and FET Structures,	Ms.K.Subbulakshmi
	AN	1.30 pm to 5 pm	Carbon Nanotube Transistors (FETs and SETs), Semiconductor Nanowire FETs and SETs,Coulomb Blockade in a Nanocapacitor, Molecular SETs and Molecular Electronics.	Ms.S.Saravana

VALUE ADDED COURSE

SCHOOL OF ELECTRICAL ENGINEERING

Fundamentals of Micro and NanoFabrication

List Of Participants

Date:01.03.2018

Sl.no	REG.NO	NAME OF THE CANDIDATE
1	U15EC002	AKHIL CHELLUBOINA
2	U15EC003	AKULA SUJITH KRISHNA
3	U15EC004	ALOK KUMAR
4	U15EC005	ALUVALA ARUN KUMAR GOUD
5	U15EC006	AMAYA E
6	U15EC007	AMBULA DEVI GOWTHAM
7	U15EC009	AMMISETTI AVINASH
8	U15EC010	ANKIT KUMAR DUBEY
9	U15EC015	ATTAR MOHAMMED TOUSIF
10	U15EC016	ATUKURI AVINASSH
11	U15EC017	BASETTY HIMABINDU
12	U15EC018	BOJJA PHANINDHRA REDDY
13	U15EC019	C. SHIVARAMAN SRIKANTH
14	U15EC020	CHANDAN PANDAY
15	U15EC021	CHAPARTHI KARTHIK
16	U15EC022	CHEKKA KESAVA PRAJWAL
17	U15EC023	CHITTIBOMMA SWATHI
18	U15EC024	DASARI HARI SAI KUMAR

19	U15EC025	DUDEKULA FAYAZ
20	U15EC026	DUDEKULA NOORNIYAZ
21	U15EC027	DUGYALA PREETHI
22	U15EC028	FAHIMA NASREEN S
23	U15EC030	GADE MOUNIKA
24	U15EC042	JAKKU MANIDEEP
25	U15EC044	JETTY SAI SUDHEER
26	U15EC046	JONNALAGADDA VENKATA MANOJ KUMAR
27	U15EC047	K O HARICHANDANA
28	U15EC050	KARICHETI BALAKRISHNA
29	U15EC051	KARNAM MOHITH
30	U15EC053	KELAM PHANI SHANKAR
31	U15EC056	KOMURAVELLI ABHILASH
32	U15EC057	KONDA ANANTH REDDY
33	U15EC058	KONDA SANDEEP
34	U15EC059	KONDAMURI VENKATESH
35	U15EC061	KONREDDY HARITHA

(Dr.M.Sangeetha)

HOD/ECE

DEPT of ECE

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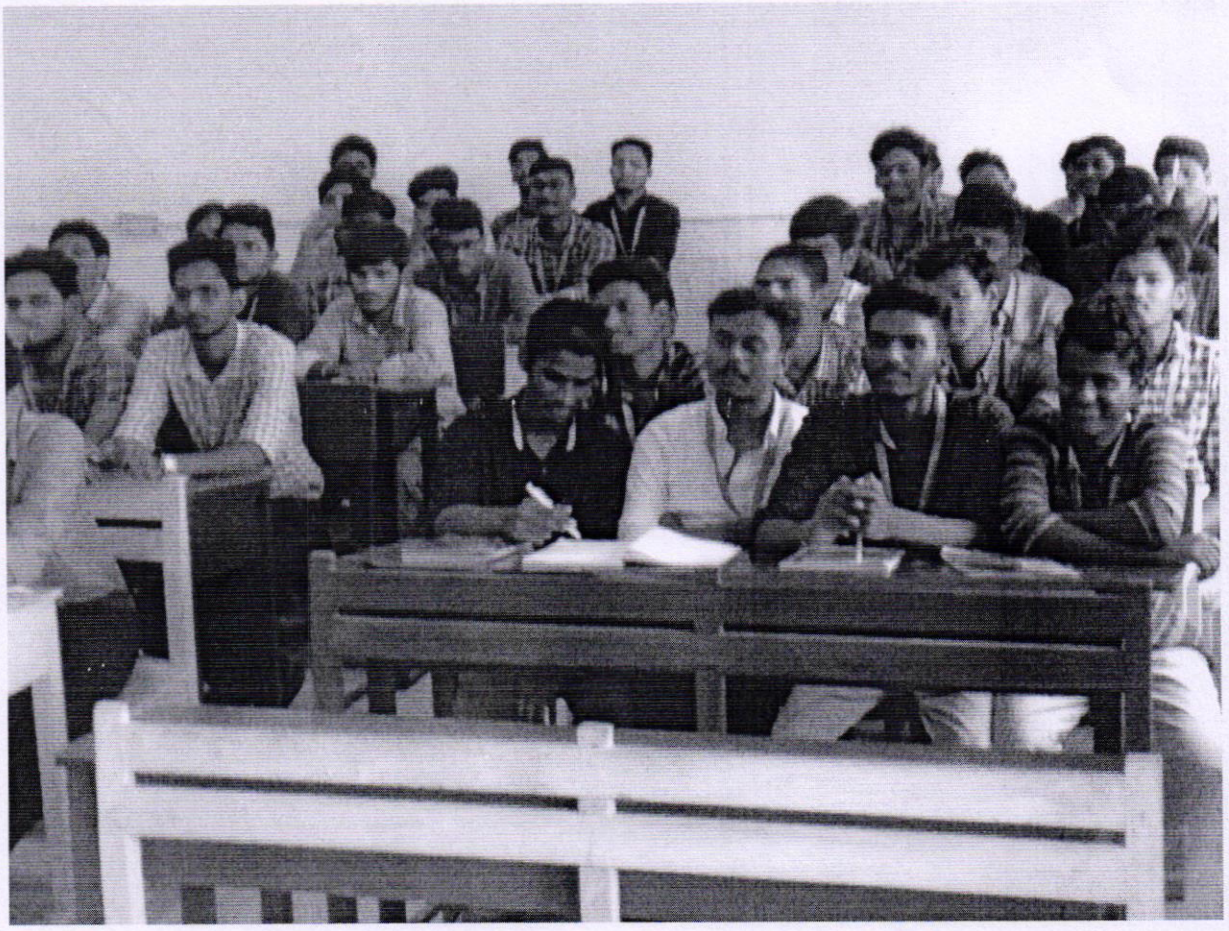
SCHOOL OF ELECTRICAL ENGINEERING

Course on Fundamentals of Micro and NanoFabrication dated on 01.03.2018 conducted by School of Electrical Engineering



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INSTITUTE OF HIGHER EDUCATION AND RESEARCH

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NAAC

ABET

SCHOOL OF ELECTRICAL ENGINEERING

CERTIFICATE OF PARTICIPATION

This is to certify that Mr / Ms **KONREDDY HARITHA(U15EC061)** has attended Value added Course On ***"Fundamentals Of Micro And NanoFabrication"*** organized by the School of Electrical Engineering, BIHER conducted from 01-03-2018 to 07-03-2018.

M.SOWMIYA MANOJ
COURSE COORDINATOR

Dr.M.SANGEETHA
CONVENOR

Bharath
INSTITUTE OF HIGHER EDUCATION AND RESEARCH
Declared as Deemed-to-be-University u/s 3 of the UGC Act, 1956

SCHOOL OF ELECTRICAL ENGINEERING

VALUE ADDED COURSE

Fundamentals of Micro and NanoFabrication

FEED BACK FORM **Date:** 07/3/2018

Name	KO. Harichandana
Register number	U15EC047
Phone number	9176415710
Email address	Hari_123@gmail.com

	Poor	Fair	Good	Very Good	Excellent
Overall Program					✓
TheSpeaker				✓	
Audio,Visual Aids Technology used					✓
Presentation hand outs				✓	

Student Signature

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VALUE ADDED COURSE

Fundamentals of Micro and NanoFabrication

FEED BACK FORM **Date:** 07/3/2018

Name	Jayant kumar
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	Poor	Fair	Good	Very Good	Excellent
Overall Program				✓	✓
TheSpeaker					✓
Audio,Visual Aids Technology used				✓	
Presentation hand outs					✓

Student Signature